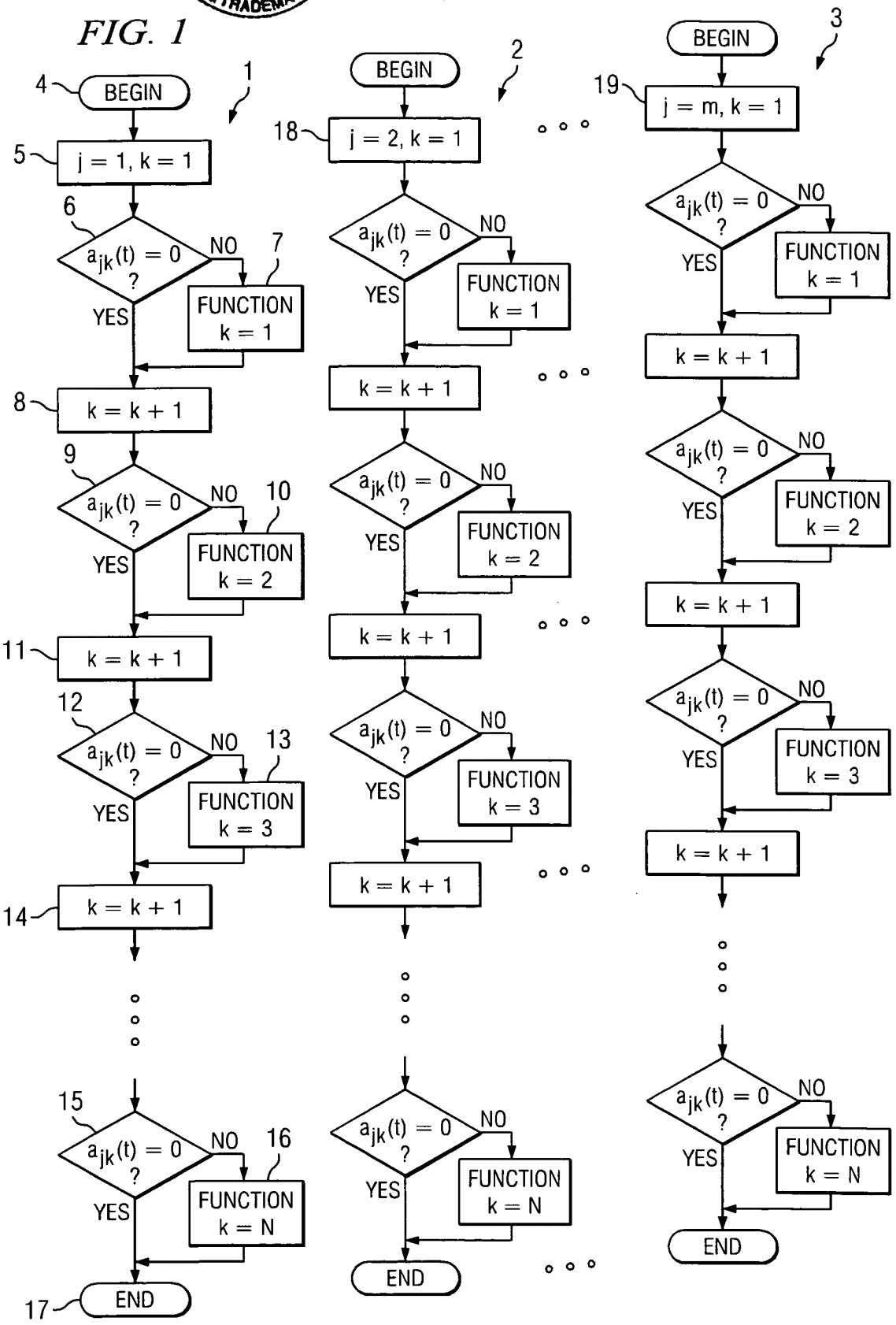
OIPE
MAR 16 2006
PATENT & TRADEMARK OFFICE

FIG. 1



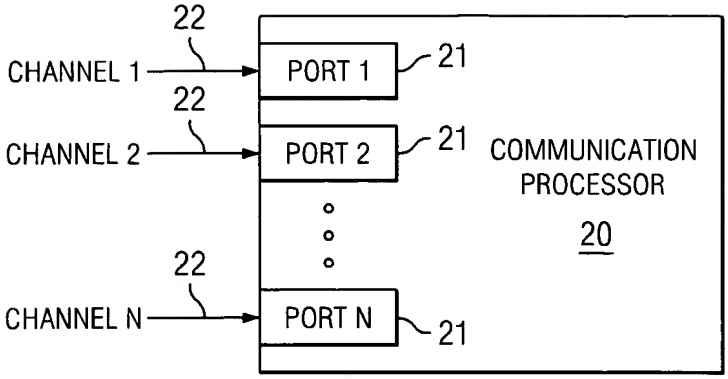

22
CHANNEL 1
PORT 1
21
22
CHANNEL 2
PORT 2
21
COMMUNICATION PROCESSOR
20
22
CHANNEL N
PORT N
21

FIG. 2

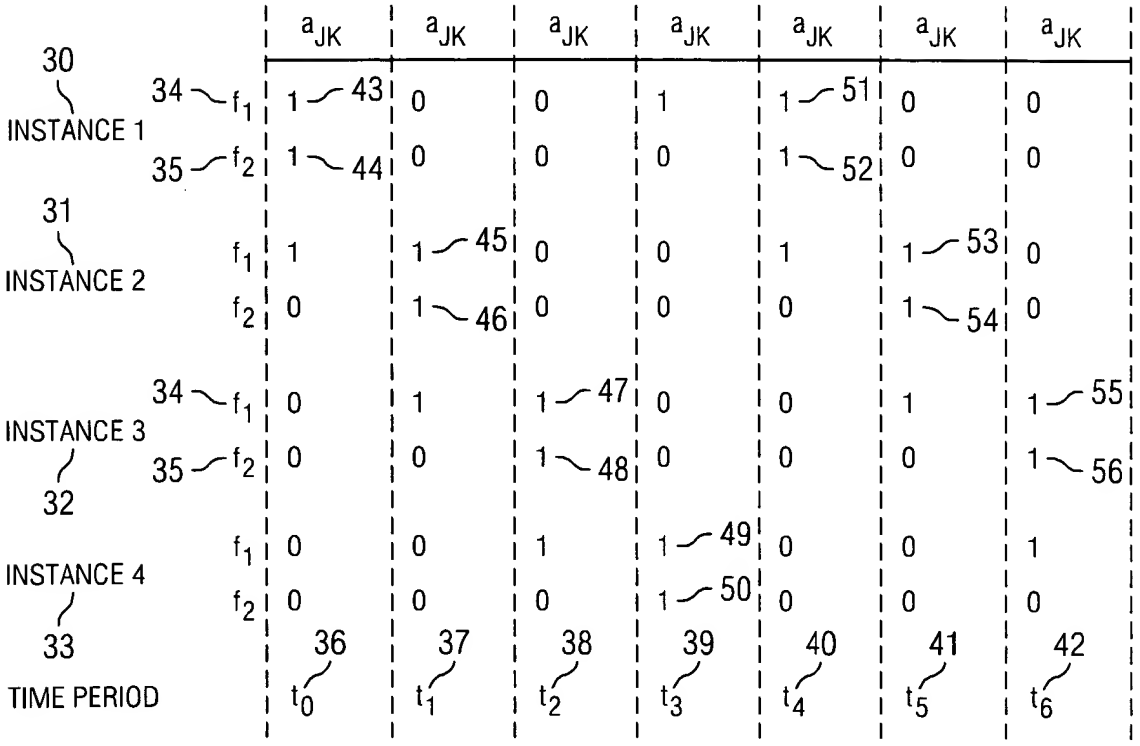

a_{JK}
30
INSTANCE 1
34 f_1 1 43 0 0 1 1 51 0 0
35 f_2 1 44 0 0 0 1 52 0 0
31
INSTANCE 2
f_1 1 1 45 0 0 1 1 53 0
f_2 0 1 46 0 0 0 1 54 0
34 f_1 0 1 1 47 0 0 1 1 55
INSTANCE 3
35 f_2 0 0 1 48 0 0 0 1 56
32
f_1 0 0 1 1 49 0 0 1
INSTANCE 4
f_2 0 0 0 1 50 0 0 0
33
36 37 38 39 40 41 42
TIME PERIOD
t_0 t_1 t_2 t_3 t_4 t_5 t_6

FIG. 3